

ABSTRACT

A plurality of interlaced video signals respectively corresponding to a plurality of continuous fields are
5 generated by first, second, and third one-field delay circuits on the basis of an inputted interlaced video signal. A first progressive video field signal is generated by a first progressive video generation circuit on the basis of the plurality of interlaced video signals. A second progressive
10 video field signal is generated by a second progressive video generation circuit on the basis of the plurality of interlaced video signals. Motion amount information in the vertical direction of a picture is calculated by a comparison circuit on the basis of the first progressive video field signal and
15 the second progressive video field signal.